

## POR33 - Power On Reset

### FEATURES

- 3.3V power supply reset
- 1V Minimum operating Voltage
- 11 $\mu$ A Total Current Consumption
- Glitch detection/rejection
- Built-in Hysteresis
- Software re-targetable (Spec. & Process)

### APPLICATIONS

- Reset of digital circuitry after power on
- Glitch Detection/Rejection

**TECHNOLOGY:** 0.18 $\mu$ m single Poly, 5 Metal Low Leakage CMOS Process

### OVERVIEW

The cell is a 3.3V Power On Reset Cell for digital circuits. The Power On Reset is monitoring the 3.3V Supply line and generates a reset when the supply voltage drops below the threshold level. The internal reference for the POR is generated by a Bandgap circuit.

### FUNCTIONAL DIAGRAM

