

LVDS500 - 500 MegaBit/s LVDS Transceiver

FEATURES

- 500MHz clocking rate
- Optional 100ohm input termination (metal mask option)
- Short-range and generic output strength modes
- Wide supply voltage operating range (2.2V < VDD < 3.6V)

OVERVIEW

Full complement of LVDS cells necessary to create a Pad ring. Includes LVDSRX, LVDSTX, LVDSREF, spacers and power supply cells. The LVDSREF is a high precision bandgap reference circuit that is used to set the proper common mode voltage.

APPLICATIONS

- High Speed general purpose signaling
- High Speed serial clocking

TECHNOLOGY: 0.13um CMOS Process

FUNCTIONAL DIAGRAM

