MOSCAD

Product Brief

VR3IC-18-80S - 3.3 to 1.8V Capless Voltage regulator

FEATURES

1.8V output ± 5%
80mA maximum Load current
Overload protection trough current limiting
Maximum load regulation drop of 10mV
Suspend mode (IDD max. 30uA)
PSRR > 30dB
Software re-targetable (Spec. & Process)

APPLICATIONS

On chip voltage regulation for analog or small digital circuits

TECHNOLOGY: 0.18um single Poly, 5 Metal CMOS Process

OVERVIEW

The cell is a high current, low voltage drop, series 3.3volt to 1.8 volt voltage regulator requiring no external capacitors. The maximum voltage drop at the full load of 80mA, does not exceed 10mV. Built-in current limiting protects the cell against overload conditions

FUNCTIONAL DIAGRAM

