

3.3V to 1.2V voltage regulator

FEATURES

- 1.2V output $\pm 10\%$
- 1mA maximum load current
- Maximum load regulation drop of 30mV
- Maximum quiescent current of 1.8 μ A
- $V_{IN} = 1.6V \dots 3.6V$
- External capacitor 1 μ F or higher (Ceramic)

APPLICATIONS

On chip voltage regulation for analog or digital circuits

TECHNOLOGY

UMC 55nm Low Leakage CMOS

OVERVIEW

The cell is a low power series 3.3V to 1.2V voltage regulator. The maximum internal current, does not exceed 1.8 μ A. The cell also shows line-and load-regulation characteristics. Its low quiescent current makes the cell ideally suited for battery operated equipment. The cell includes an ultralow-power bandgap reference circuit.

BLOCK DIAGRAM

