

### 13-Bit 2.56MS/s Successive Approximation ADC

#### FEATURES

- Very Low Power consumption of 5mW@2.56MS/s)
- INL of +/- 0.5 LSB (Typical)
- DNL of +/- 0.5LSB (Typical)
- SNR > 72dB @ Fin=250kHz
- SFDR > 70dB @ Fin=250kHz
- Differential FS Input of +-1.6V
- Standard CMOS Technology (No MiM, No double Poly options needed)

#### APPLICATIONS

High Speed, High resolution ADC applications for battery operated equipment like Digital Cameras and Video applications

**TECHNOLOGY:** 0.18u CMOS Technology

#### OVERVIEW

The ADC13-2M56 is a 13-Bit, 2.56MS/s Successive Approximation Analog-to-Digital converter ideally suited for automotive, industrial or robotic applications. Its very low power consumption and small footprint, makes it well suited for portable, battery operated devices.

#### FUNCTIONAL DIAGRAM

